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In re Application of: William H. Thesling

Application No.: 09/954,698

Art Unit: 2133

Filed: September 11, 2001

Examiner: Phung M. Chung

For: Soft Input-Soft Output Forward Error  
Correction Decoding for Turbo Codes

Atty Docket No.: 098417-610011

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Statement is submitted in compliance with 37 C.F.R. § 1.56.

A list of patent(s) and/or publication(s) is set forth on the attached Form PTO-1449. Copies of the listed items are enclosed, if required.

No fee is believed to be due for entry of this Information Disclosure Statement. However, if any fee should be required, please charge such fee to Jones Day's Deposit Account No. 501432, Reference No. 098417-610011.

Respectfully submitted,

Date

3/14/05

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Enclosures

Approved for use through 07/31/2006. OMB 0651-0031

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

**Complete if Known**

Application Number	09/954,698
Filing Date	September 11, 2001
First Named Inventor	William H. Thesling
Art Unit	2133
Examiner Name	Phung M. Chung
Attorney Docket Number	098417-610011

Sheet	1	of	2
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## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

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Examiner Signature		Date Considered	
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		Application Number	09/954,698
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Sheet	2	of	2

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		R. Pyndiah, P. Combelles and P. Adde, "A very low complexity block turbo decoder for product codes," in proc. of IEEE 1996, pp. 101-105.	
		A. Picart and R. Pyndiah, "Performance of Turbo-Decoded Product Codes Used in Multilevel Coding," in proc. of IEEE ICC'96 Conference, Dallas TX 1996, pp. 107-111	
		R. Pyndiah, A. Glavieux, A. Picart and S. Jacq, "Near optimum decoding of product codes," in proc. of IEEE Globecom '94 Conference, vol. 1/3, Nov.-Dec. 1994, San Francisco, pp. 339-343.	
		R. Pyndiah, "Near-optimum decoding of product codes: Block turbo codes, IEEE Transactions on Communications, Vol. 46, No. 8, August 1998, pp. 1003-1010	

Examiner Signature	Date Considered
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